

Release Note for VigorSwitch P1282

Firmware Version:	2.9.10
Loader Version:	3.5.3
Release Type:	Important – Review release notes and upgrade if the changes affect your system stability, performance, or security
Applied Models:	VigorSwitch P1282

VigorSwitch P1282, a PoE Gigabit Ports Web Smart Switch with 24 ports + 4 Combo UTP/SFP ports, is a standard switch that meets both IEEE 802.3u/ab Fast Ethernet and Gigabit Ethernet specifications.

New Features

- None.

Improvement

- Improved: Improve the CGI security (CVE-2026-52497, CVE-2026-52498, CVE-2026-52499, CVE-2026-52500, CVE-2026-52501, CVE-2026-52502 and CVE-2026-52503).
- Improved: Incorporating the security patches to OpenSSL.
- Improved: Add support for including the device hostname in DHCP Option 12 when sending DHCP requests.
- Improved: Dashboard load time on the switch by deferring the VlanManagement client data fetch and loading port status information only on demand.
- Corrected: An issue where the switch rebooted when clearing the Protected Host Table while the MAC table contained a large number of entries.
- Corrected: An issue where stale MAC/ARP entries remained on the L3 switch after an HA VIP failover, causing traffic to be forwarded to the previously active node.
- Corrected: An issue where intermittent L3 VLAN routing failures caused end devices to lose connectivity under high network load when using the switch as their default gateway.
- Corrected: An issue where a DHCP Server IP interface with the Pool Count set to 1021 generated an error and could not be saved.
- Corrected: An issue where SFP vendor information was displayed as "undefined" in the Dashboard on the switch due to incorrect handling of combo port data.
- Corrected: An issue where LAG (LACP) ports were not displayed in the MAC Address Table.

Known Issue

- Users could not downgrade the firmware below 2.8.x, if the loader version is 3.5.2.

Note

- STP (Spanning Tree Protocol) is enabled by default starting from firmware version 2.9.8, which may result in a slightly longer link-up time for ports.